



IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Nicholas J. Kelsey, et al.
SERIAL NO.: 09/748,098
FILING DATE: December 21, 2000
TITLE: System and Method for Instruction Level Multithreading in an Embedded Processor Using Zero-Time Context Switching
EXAMINER: Charles A. Harkness
GROUP ART UNIT: 2183
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CERTIFICATE OF MAILING

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Dated: July 19, 2004

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**REQUEST FOR CONTINUED EXAMINATION UNDER 37 C.F.R. § 1.114 AND
AMENDMENT B AND RESPONSE AS SUBMISSION THEREWITH**

Sir:

The following is submitted as part of a Request for Continued Examination under 37 C.F.R. § 1.114. This Amendment B and Response for the patent application identified above is in response to the Final Office Action dated April 20, 2004, which set a shortened statutory period for response that expires on July 20, 2004.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.

Kindly amend this application as indicated herein.